



501.39835CX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. HASEGAWA, et al.

Application No.: 10/627,769

Filed: July 28, 2003

For: A SEMICONDUCTOR MEMORY DEVICE HAVING
IMPROVED ARRANGEMENT FOR REPLACING FAILED
BIT LINES (As Amended)

Art Group: Unknown

Examiner: Unknown

SUPPLEMENTAL PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 23, 2003

Sir:

Prior to examination, and in supplement to the Preliminary Amendment filed on July 28, 2003, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to Claims; and

Remarks are included following the amendments.